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### **Application Information**

Title Line One:: SEMICONDUCTOR MANUFACTURING METHOD  
Title Line Two:: AND SEMICONDUCTOR MANUFACTURING  
Title Line Three:: APPARATUS  
Title Line Four::

Total Drawing Sheets:: 33  
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**Continuity Information**

>This application is a:: Divisional  
Application One:: 09/544,463  
Filing Date:: April 7, 2000  
Patent Number::  
which is a::  
>>Application Two::  
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**Prior Foreign Applications**

Foreign Application One:: 11-99562  
Filing Date:: April 7, 1999  
Country:: Japan  
Priority Claimed:: Yes  
Foreign Application Two::  
Filing Date::  
Country::  
Priority Claimed::  
Foreign Application Three::  
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Country::  
Priority Claimed::